Digital Interface For Use With Audio Cassette Tape Recorder.

Mark Wilson.

3/10/79.

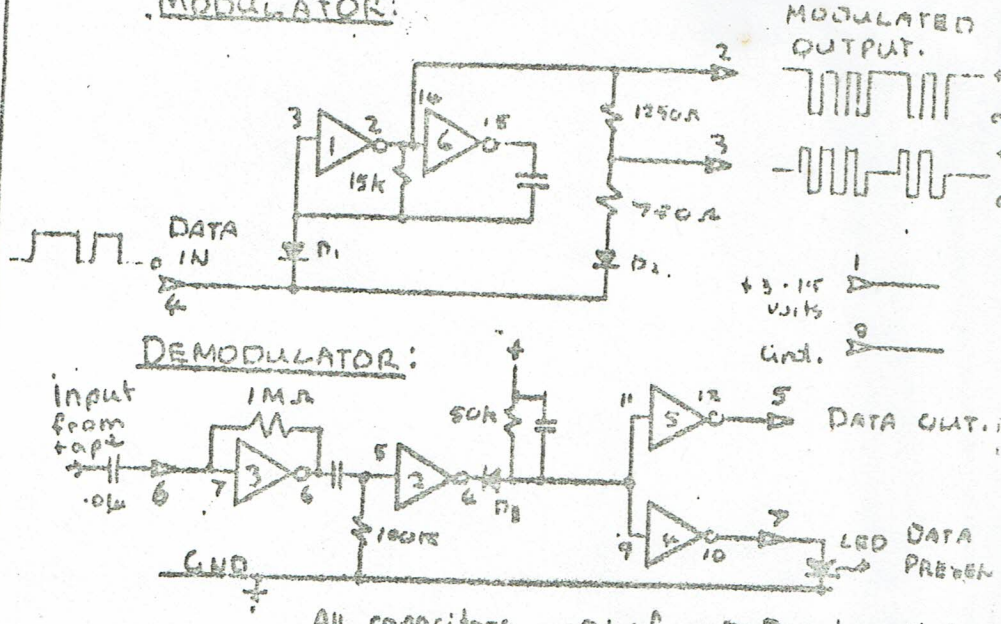
Pastes:
Conductors: 7635
Resistors: 3913
3916

Firing Data.
Peak temp: 850°C.
Screen Mesh: 186

Print thickness.

		Dried	Fired
Res.	.01	27μ	18μm.
	.02	36μ	24μm.
Cond.	.02	32μ	16μ
	.01		17μ
	.06	53μ	23μ

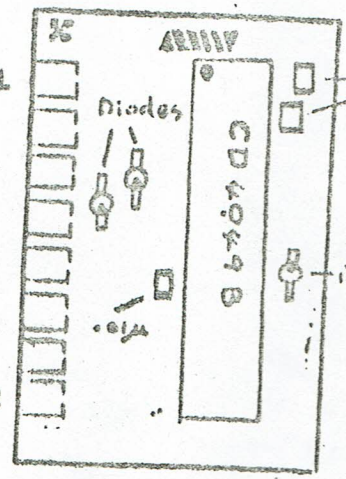
Circuit Diagram.



Values.

Target	Measured. 1	2	3
750	621	645	624
1290	1107	1106	1105
15k	21.8k	21.8k	22.6k
100k	107k	113k	101k
1M.	1.9μM	1.7M	2.1M

Component Layout.



Add On Components.

1 x CD4049B Hex Inverter.

3 x .01μF Vitramon capacitors

3 x MMD 6050 Diodes ML241.

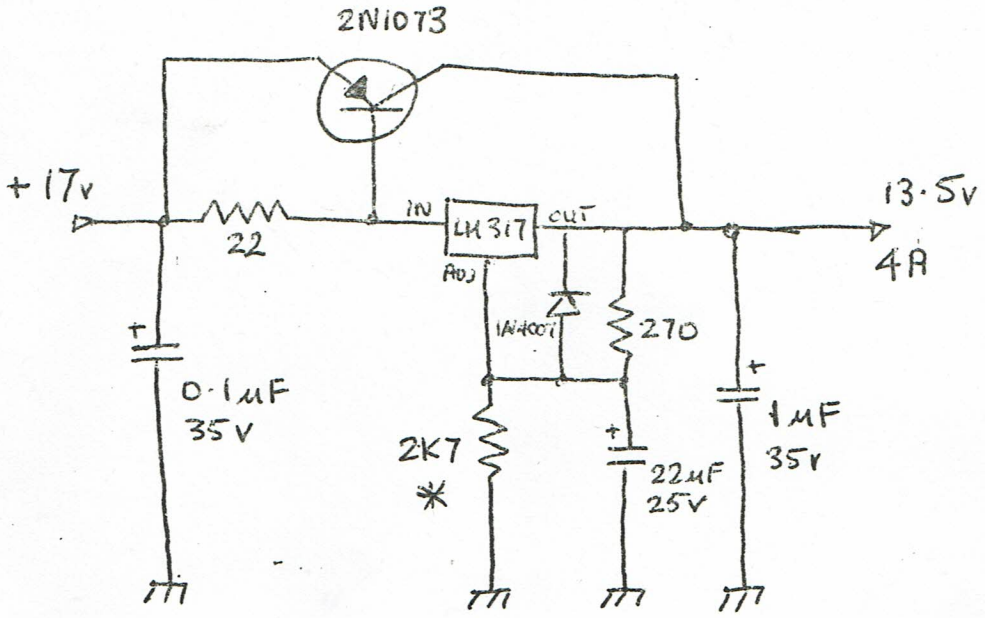
8 x Dil. Pins.

Approximate cost. $3.20

Circuit Description.

Modulator: This is formed from a simple 2 inverter oscillator which is turned on and off by the incoming data. Its on frequency is approx 2.5 kHz. D2 holds the output at ~2.5 volts when the input is low.

Demodulator: Inverter 3 acts as an amplifier. Its output is high pass filtered and squared up by Inv. 2. D3 and its associated resistor and capacitor act as a leaky diode pump demodulator. Inverters 4 and 5 buffer the output. Both outputs are identical. One can be used



* VALUE MAY BE CHANGED UPTO 5K TO VARY O/P VOLTS.

DC REGULATOR 4 AMP
1·5 - 30V OUTPUT